**FIT**

.org 0x1010 #get IER

b FITCODE #get mer

.org 0x3000

lis r1 0x03c0

li r0,0

mttcr r1

mttsr r0

li r30, 0x8448

mtevpr, r0

wrteei 1

here: b here

FITCODE:

lis r1, 0x400

mttsr r1

addi r27, r27, 1

stw r27, 0x0(r30)

rfi

**PIT**

org 0x1000 #get IER

b PITCODE #get mer

.org 0x3000

.set PERIOD, 0x80000000

lis r10, PERIOD@h

li r0,0

mtpit r10

lis r1 0x03c0

mttcr r1

mttsr r0

li r30, 0x8448

stw r0, 4(r30)

mtevpr, r0

wrteei 1

here: b here

PITCODE:

lis r1, 0x0800

mttsr r1

addi r27, r27, 1

stw r27, 0x0(r30)

rfi

**ISR**

.set IPR, 4

.set IAR, 0xC

.org 0x0500 # ISR address

lis r20, 0x8234 # point at interrupt controller

lis r24, 0x8244 # point at timer module

lwz r21, IPR,(r20) # get pending register

andi r22,r21,0x8 # check on bit

beq go\_back # not set, get out

li r24, 0x106 # SEE BELOW JERAMIE BITS IN TIMER (for JERAMIE)

stw r29, 0(r24) # send to TCR0 21 22 23 24 25 26 27 28 29 30 31

li r25, 0x8 # get bit for flag 0 0 1 1 1 0 1 0 1 1 1

stw r25,IAR(r20) # reset bit in interrupt controller

lis r26, 0x8256 # now point at LEDs

addi r23, r23,1 # bump pointer

stw r22,0(r26) # send out

go\_back: rfi # return from interrupt